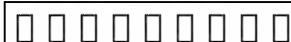


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# WM-8330A

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









































































































































1. 本装置は、**VP-8002 (Ver. 1.0)** のソフトウェアを使用し、**PLL** (Phase-Locked Loop) 回路を介して、**800 MHz** の周波数を生成します。  
 2. 1 秒あたり **20 回** (20%) の割合で、**5 分** (5 分) の間、動作します。  
 3. 本装置は、**1 分** (1 分) の間、動作します。

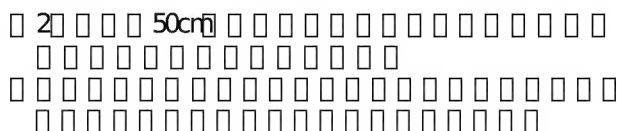
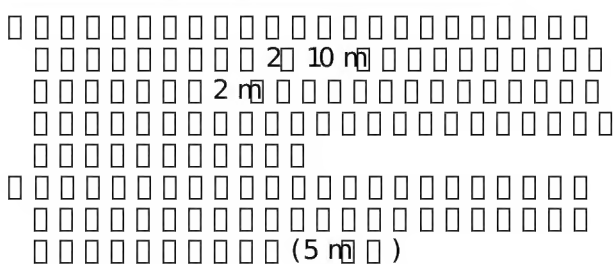
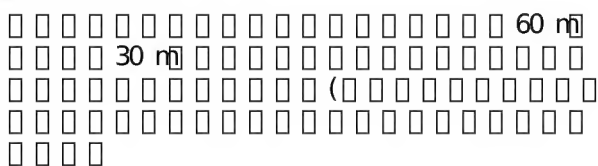
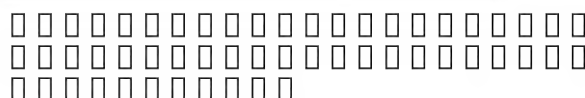
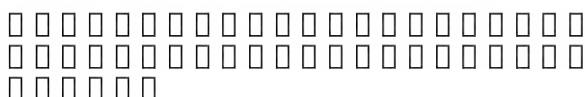
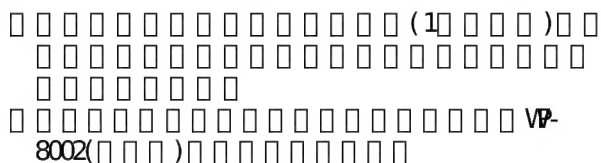
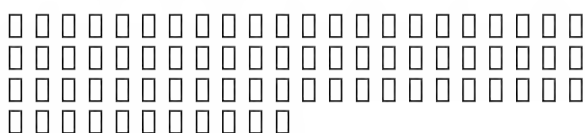
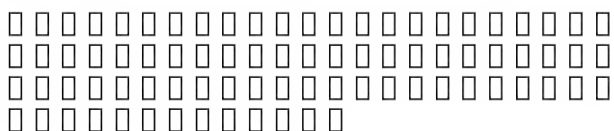
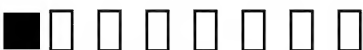
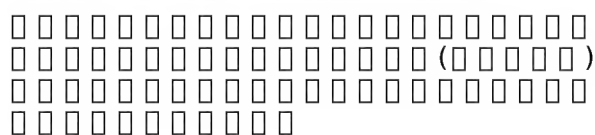
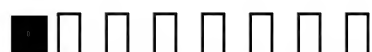
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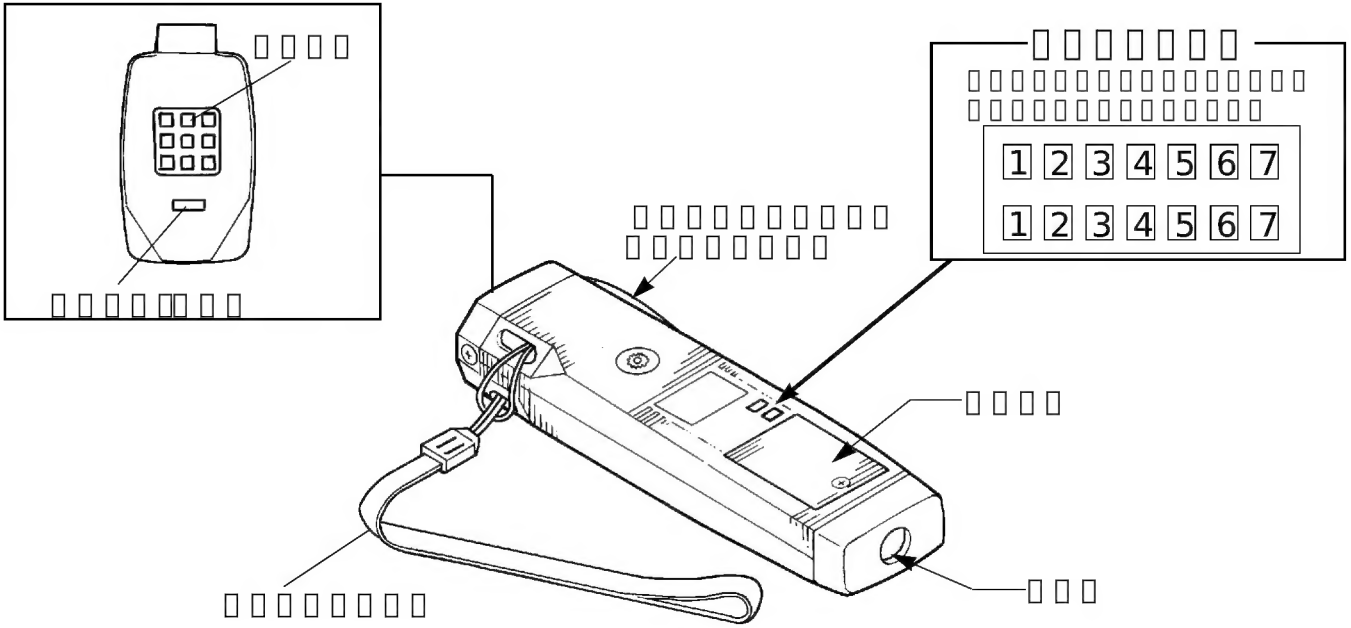


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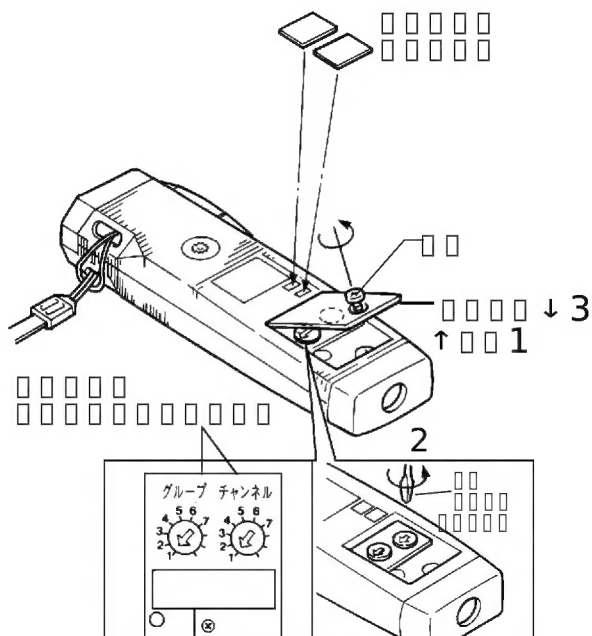
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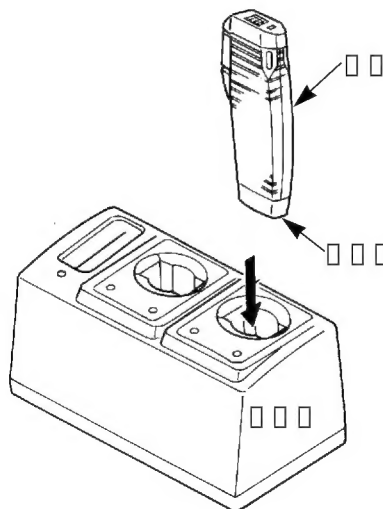
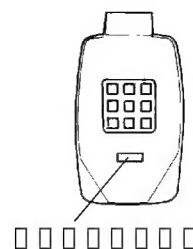
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